

APPLICANT'S ART CITATION (Use several sheets if necessary)		Application Not Known	OFGS File No. P/1071-1480										
		Applicant Yutaka SASAKI, et al.											
		Filing Date Not Known	Group Art Unit Not Known										
U.S. PATENT DOCUMENTS													
Examiner Initial	Document Number			Date	Name	Class	Sub-class	Filing Date If Appropriate					
FOREIGN PATENT DOCUMENTS													
	Document Number				Date	Country	Class	Translation					
								Yes	No				
SJ	0	7	8	6	8	0	2	3/31/1995	JAPAN			JAPANESE ABSTRACT	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)													
SJ		Article IEEE MTT-S, Murat Manufacturing Co., Ltd., "Multi-chip Transmitter/Receiver Module Using High Dielectric Substrates for 5.8 Ghz ITS Applications" May 1999											
Examiner	<i>Stephen C. Jen</i>		Date Considered		4-24-03								
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